

Electronic Acknowledgement Receipt

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| EFS ID: | 9743445 | | |
| Application Number: | 10653237 | | |
| International Application Number: | | | |
| Confirmation Number: | 4842 | | |
| Title of Invention: | SEMICONDUCTOR DEVICE INCLUDING TRANSISTOR WITH COMPOSITE GATE STRUCTURE AND TRANSISTOR WITH SINGLE GATE STRUCTURE, AND METHOD FOR MANUFACTURING THE SAME | | |
| First Named Inventor/Applicant Name: | Katsuki Hazama | | |
| Correspondence Address: | <p>B. Noel Kivlin Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. P.O. Box 398 - Austin TX 78767-0398 US 512-853-8800 -</p> | | |
| Filer: | Dean M. Munyon/Christine Acker | | |
| Filer Authorized By: | Dean M. Munyon | | |
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| File Listing: | |

| Document Number | Document Description | File Name | File Size(Bytes)/Message Digest | Multi Part/.zip | Pages (if appl.) |
|--|---|------------------|---|-----------------|------------------|
| 1 | Power of Attorney | 6913973_POA.pdf | 2889782 ad17857d886342ae8bb634c7347d1bf12d b9fb | no | 2 |
| Warnings: | | | | | |
| Information: | | | | | |
| 2 | Assignee showing of ownership per 37 CFR 3.73(b). | 6913973_373b.pdf | 2641515 9738bc0fb180066fd3aa34ac9922c6d99c ae33 | no | 1 |
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| Total Files Size (in bytes): | | | | 5531297 | |
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